Considering the directory protocol for distributed shared memory, please associate proper statements to the following state transition diagram for the directory. Note that $P$ in the following statements means the requesting processor number. (14%) (Note that each item has 2 points)

A) Write Miss:
   send Fetch/Invalidate;
   send Data Value Reply msg to remote cache;
   Sharers = \{P\};

B) Write Miss:
   send Invalidate to Sharers;
   then Sharers = \{P\};
   send Data Value Reply msg

C) Write Miss:
   Sharers = \{P\};
   send Data Value Reply msg

D) Read miss:
   Sharers += \{P\};
   send Data Value Reply

E) Read miss:
   Sharers = \{P\}
   send Data Value Reply

F) Read miss:
   Sharers += \{P\};
   send Fetch;
   send Data Value Reply msg to remote cache (Write back block)

G) Data Write Back:
   Sharers = {} (Write back block)
2. Suppose we summarize the cache optimization techniques with "+" meaning that the technique improves the factor, "−" meaning it hurts that factor, and blank meaning it has no impact. For example, items (1), (2), (3), and (4) are all blank. Given the following table, please identify which items are "+" and which items are "−" (in an increasing order). (Note that give the correct/incorrect answer for an item, get/lose 1 point; otherwise, no point. The maximum points got are 16; the minimum points got are 0.) (16%)

<table>
<thead>
<tr>
<th>Technique</th>
<th>Hit time</th>
<th>Bandwidth</th>
<th>Miss penalty</th>
<th>Miss rate</th>
</tr>
</thead>
<tbody>
<tr>
<td>Banked caches</td>
<td>(1)</td>
<td>(12)</td>
<td>(23)</td>
<td>(34)</td>
</tr>
<tr>
<td>Compiler techniques to reduce cache miss</td>
<td>(2)</td>
<td>(13)</td>
<td>(24)</td>
<td>(35)</td>
</tr>
<tr>
<td>Compiler-controlled prefetching</td>
<td>(3)</td>
<td>(14)</td>
<td>(25)</td>
<td>(36)</td>
</tr>
<tr>
<td>Critical word first and early restart</td>
<td>(4)</td>
<td>(15)</td>
<td>(26)</td>
<td>(37)</td>
</tr>
<tr>
<td>Hardware prefetching of instructions and data</td>
<td>(5)</td>
<td>(16)</td>
<td>(27)</td>
<td>(38)</td>
</tr>
<tr>
<td>Merging write buffer</td>
<td>(6)</td>
<td>(17)</td>
<td>(28)</td>
<td>(39)</td>
</tr>
<tr>
<td>Nonblocking caches</td>
<td>(7)</td>
<td>(18)</td>
<td>(29)</td>
<td>(40)</td>
</tr>
<tr>
<td>Pipelined cache access</td>
<td>(8)</td>
<td>(19)</td>
<td>(30)</td>
<td>(41)</td>
</tr>
<tr>
<td>Small and simple cache</td>
<td>(9)</td>
<td>(20)</td>
<td>(31)</td>
<td>(42)</td>
</tr>
<tr>
<td>Trace caches</td>
<td>(10)</td>
<td>(21)</td>
<td>(32)</td>
<td>(43)</td>
</tr>
<tr>
<td>Way-prediction cache</td>
<td>(11)</td>
<td>(22)</td>
<td>(33)</td>
<td>(44)</td>
</tr>
</tbody>
</table>

3. Assume a disk subsystem with the following components and FIT:
- 12 disks, 1,000 FIT
- 1 SCSI controller, 2,000 FIT
- 1 power supply, 5,000 FIT
- 1 fan, 5,000 FIT
- 1 SCSI cable, 1,000 FIT

Using the simplifying assumptions that the lifetimes are exponentially distributed and failures are independent, please compute the MTTF of the system as a whole. (5%)

4. Given a graph $G = \langle V, E \rangle$, the following is an algorithm to find a minimum spanning tree of $G$. Can you improve the algorithm for better efficiency? Some explanation is necessary! (10%)

```plaintext
MST(G)
1 $T = \emptyset$
2 while ((E not empty) {  
3     choose an edge \((v, w)\) from \(E\) of lowest cost;  
4     if \((v, w)\) does not create a cycle in \(T\) \(T = T \cup (v, w)\);  
5     else discard \((v, w)\);  
6 }
```
5. Answer the following two questions about binary search tree (BST):
(a) The following is a BST with no other extra information. To search a number \( a \) on this BST, it takes \( n \) steps (\( n \) comparisons) to know \( a \) does not exist in this tree. What is the minimum and maximum of \( n \)? List one of the possible search sequences in each of the two cases. (10%)

(b) Following part (a), what is the relation between \( a_6 \) and \( a_7 \)? (3%)
(c) (Independent from part (a)) Rearrange the following sequence of seven numbers (if there is a need), 10, 2, 30, 14, 5, 6, 7 to put into a BST from an empty tree so that the tree depth of the final tree after all the insertions is as small as possible; also, the rearrangement must take as few steps from the original ordering as possible. List the sequence after the rearrangement and draw the final tree after the insertions. (7%)

6. Answer the following question regarding virtual memory:
(a) List at least three advantages and one drawback of demand paging technique comparing to the classic approach of loading the entire program into main memory when launching a program. (10%)
(b) Explain what is thrashing and how to prevent thrashing from happening. (6%)

7. In shortest-job-first (SJF) scheduling, it is common to use exponential average as the predicted next burst time. Let \( t_i \) denote the measured \( i \)th burst time, \( n_i \) denote the \( i \)th guest rounding to the nearest integer.
(a) Given \( t_1 = 6, t_2 = 13, n_1 = 1, n_2 = 5, n_3 = 12 \), what is the possible range of the parameter \( \alpha \) (10%)?
(b) Explain why SJF could result in process starvation and how do you suggest to solve this problem (9%)?